ABSTRACT

A power system having a power factor correction circuit includes a power circuit. The power circuit includes first and second input capacitors, first and second coupled inductors, first and second switches, and an output circuit. The first and second input capacitors form a bank node. The first and second switches are connected to the first and second inductors and the bank node. Likewise, the first an second input capacitors are connected to the first and second inductors. The output circuit is operable to produce an output voltage. A control circuit is operable to monitor the output voltage and produce a switch signal. A drive circuit is coupled to the control circuit and the first and second switches and is operable to operate the first and second switches based on the switch signal.

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